Total Output Power: 450-550 Watts +12 Vdc Main Output +3.3 Vdc Stand-by Output Wide Range Input Voltage: 90 264 Vac

## SPECIAL FEATURES

- Active power factor correction
- EN61000-3-2 harmonic compliance
- Inrush control
- 1U X 2U form factor
- 10.3 W/in ${ }^{3}$ (DS550) 8.4 W/in ${ }^{3}$ (DS450)
- +12 Vdc output
- +3.3 Vdc stand-by
- No minimum load required
- Hot plug operation
- N + 1 redundant
- Internal OR'ing fets
- Active current sharing
- Built-in cooling fans ( $40 \mathrm{~mm} \times 28 \mathrm{~mm}$ )
- $I^{2} \mathrm{C}$ communication Interface bus
- EEPROM for FRU data
- Amber LED status, fan_fail
- Green LED status, power good/AC_OK status
- Internal fan speed control
- Fan fail tach output signal
- One year warranty


## SAFETY

- UL/cUL 60950 (UL recognized)
- NEMKO+ CB report EN60950
- EN60950
- CE mark
- China CCC


## Distributed Power Bulk Front-End



| Logic Control | When supply is inserted into the system the pin is pulled LOW and power supply is ON after all other pins are seated |
| :--- | :--- |
| PS Inhibit: | $I^{2} \mathrm{C}$ port P6. When the power supply is on and running normal P6 is low. When the power supply is off, either due <br> to -PS_ON, PS_KILL, or a fault, then P6 is high. |
| PS_Status: | $I^{2} \mathrm{C}$ port P7. P7 is high except when the power supply turns the main outputs, not +3.3 Vsb, off due to an AC failure <br> (AC missing or too low for power supply operation). If the supply is turned off due to -PS_ON, PS_KILL, or a fault, <br> then P7 remains high. |
| AC_Pfail: | The PSU will provides an open collector Tach 1 output. |
| Fan_Fault: | This signal is generated from the fan. The signal should generate 2 pulses per revolution. The logic in the system <br> will be operating at 3.3 V. |
| Tach_1: |  |

## Environmental Specifications

| Operating temperature: | $-10^{\circ} \mathrm{C}$ to $50^{\circ} \mathrm{C}$ |  |  |
| :--- | :--- | :---: | :---: |
| Storage temperature: | $-40^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |  |  |
| Altitude, operating: | $10,000 \mathrm{ft}$. |  |  |
| Electromagnetic susceptibility/Input transients: | -EN61000-3-2, $-3-3$ <br> - -EN61000-4-2, 4.3, 4-4, -4-5, 4-11 <br>  <br> -EN55024:1998 |  |  |
| RoHS \& lead-free compliant (no tantalum caps) | 20 to $90 \%$ RH, non-condensing |  |  |
| Humidity: | Shock and vibration specificatons complies with Artesyn Embedded Technologies Std. Specification.  <br> MTBF (Demonstrated): $400 \mathrm{~K} \mathrm{Hrs} \mathrm{at} \mathrm{full} \mathrm{load}, 40^{\circ} \mathrm{C}$ l |  |  |

Ordering Information

| Output | Nominal Output <br> Voltage Set Point | Set Point <br> Tolerance | Total <br> Regulation | Minimum <br> Current | Maximum <br> Current | Output Ripple <br> P/P | Over Current |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DS450-3 | 12.0 Vdc | $\pm 0.2 \%$ | $+5 /-3 \%$ | 0 A | 37.0 A | 120 mV | $39.5 \mathrm{~A}-44.4 \mathrm{~A}$ <br>  $\mathrm{3.3Vsb}$ |

*Overcurrent latches off if overcurrent lasts over 1 second, otherwise it is auto recovery.
*For 5 Vsb, please contact marketing department.

## Mechanical Drawings



## DC Output Connector Pinout Assignment

## Male connector as viewed from the rear of the supply:

| D1 | D2 | D3 | D4 | D5 | D6 | PB1 | PB2 | PB3 | PB4 | PB5 | PB6 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| C1 | C2 | C3 | C4 | C5 | C6 |  |  |  |  |  |  |
| B1 | B2 | B3 | B4 | B5 | B6 |  |  |  |  |  |  |
| A1 | A2 | A3 | A4 | A5 | A6 |  |  |  |  |  |  |

## P1 - Power Supply Side

| 1 | FCI Power Blade 51721 series <br> $51721-10002406 A A$ |
| :--- | :--- |
| 2 | Molex Power Connector <br> SD-87667 series <br> $87667-7002$ |

## Pin Assignments

| Pin | Signal Name |
| :--- | :--- |
| PB 1 | +12 V Return |
| PB 2 | +12 V Return |
| PB 3 | +12 V Return |
| PB 4 | +12 V |
| PB 5 | +12 V |
| PB 6 | +12 V |
| A1 | PS_KILL |
| A2 | +12 V _Current Share |
| A3 | Logic Return |
| A4 | +3.3 V Stand-By |
| A5 | +3.3 C Address BIT 0 Signal) |
| A6 | Logic Return |
| B1 | Spare |
| B2 | Logic Return |
| B3 | +3.3 V Stand-By |
| B4 | SDA (I2C Data Signal) |
| B5 | PSON (Power Enable Signal) |
| B6 |  |

## Mating Connector (System Side)

| 1 | FCI Power Blade <br> $51741-10002406 C C$ <br> Strait Pins |
| :--- | :--- |
| 2 | FCI Power Blade <br> $51761-10002406 A A$ <br> Right Angle |

Pin Assignments

| Pin | Signal Name |
| :--- | :--- |
| C1 | Logic Return |
| C2 | Tach_1 (Fan Fail Signal) |
| C3 | Logic Return |
| C4 | +3.3 V Stand-By |
| C5 | SCL (I2C Clock Signal)* |
| C6 | VIN_GOOD (AC Input present) |
| D1 | -PS_Present (Power Supply Seated) |
| D2 | Spare |
| D3 | Logic Return |
| D4 | +3.3 V Stand-By |
| D5 | S_INT (Alert) |
| D6 | POK (Output Power Ok) |

*Supports $\mathrm{I}^{2} \mathrm{C}$ standard mode (100 kHz) only

